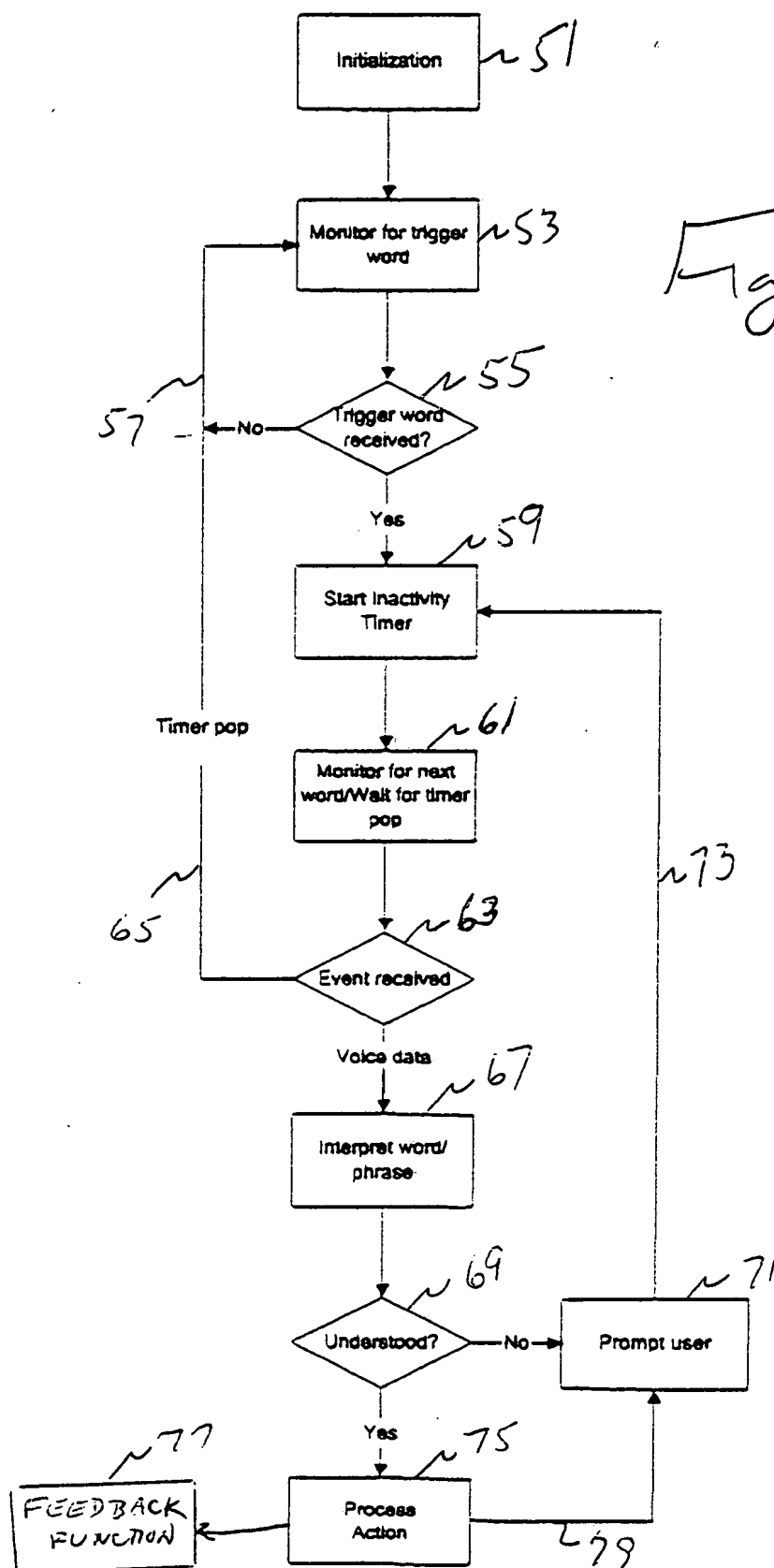


The diagram illustrates a speech processing system architecture. A central vertical bar represents the **Processor**. To its left, several components are connected: a **Proximity Sensor** (labeled 17) connects to the top of the Processor; a **Microphone** (labeled 21) connects to an **Audio Controller** (labeled 19), which in turn connects to a **Voice Recognition Unit** (labeled 23); the **Voice Recognition Unit** is connected to **Memory** (labeled 25) and the Processor; a **Text to speech generation** unit (labeled 29) is connected to the Processor and the **Audio Controller**, which then connects to a **Speaker** (labeled 33); a **Display Controller** (labeled 35) is connected to the Processor and a **Display** (labeled 37); and a **Keyboard for configuration** (labeled 39) connects to the bottom of the Processor. To the right of the Processor, a **Data Storage** unit (labeled 31) is connected via a bidirectional arrow. Handwritten annotations include squiggly lines and numbers (17, 21, 19, 23, 25, 29, 33, 37, 35, 39) pointing to their respective components.

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Figure 3



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